



Advanced Metallization Conference 2022: 31st Asian Session

October 12 - 14, 2022: Hybrid Conference (on site & online)

Sponsorship by the Japan Society of Applied Physics

The ADMETA^{Plus} is heading for the 31st meeting and has a long record of important contributions to practical progress for advanced MPU and various memory devices. In recent years, the importance of interconnect technology for realizing low resistance, large integration, rich function, low cost, and high reliability is increasing in various device application fields beyond silicon electronics.

This conference is focusing interconnects technology and science related to materials, processes, device design, assembly, equipment, cost performance, and characterization. We will conduct comprehensive discussions widely from basics to applications with researchers and engineers from industry, government, and academia. We are looking forward to new developments in interconnect technology fields and contributing to the growth of semiconductor industry in the Asian area. Due to the infection status of the COVID-19, ADMETA^{Plus} 2022 will be held virtually.

ADMETA^{Plus} 2022 General Chair, Hideaki Machida (Gas-Phase Growth LTD.)

★Conference Topics of Interest

Integration: Interconnection Structure and Performance, Parasitic Capacitance, Reliability Technology, Testing and Analysis

Reliability Science and Failure Analysis: EM, SIV, Tddb, Defect Detection and Analysis, Failure Mechanism and Modeling

Metallization: PVD, CVD, ALD, Plating, Barrier Metal, New Material, Alloy, Supercritical Fluid, Reflow

Low-k Dielectric: CVD, ALD, SOD, Film Properties, New Materials, Dielectric Structures (Air Gap), Metrology, etc.

CMP: Planarization Technology, Slurry, Pad, Dress, End Point Detection, Cleaning, Anti-corrosive Technology, etc.

Contact: Silicide, Interface, Solid Phase Reaction, Shallow Junction, Crystal Properties, Electron Properties, Carrier Transport, etc.

MEMS/RF: Interconnection Structure and Materials, Packaging, Fabrication Process Technology, Device Design, etc.

Emerging Technology: Active Wiring, Power Electronics, Silicon Photonics, Flexible Electronics, Energy Harvesting, etc.

Backend Device Technology: Tech. for Embedding Devices (MRAM, PCRAM, ReRAM, DRAM etc.), Materials and Processing of Magnetics, Phase-Change and Resistive-Change Devices, Electrode, Metallization, etc.

Nano Carbon: Graphene, CNT, Deposition, Integration, Electrical Characteristics, Reliability, Evaluation, Analysis, etc.

3D and Packaging: TSV, TMV, Stacking Method (CoW, WoW), Thinning, Planarization, Bonding, Bump, Stress and Thermal Analysis, Sealing, Cooling, Reliability.

★ To Apply:

Prepare an abstract per the directions below and submit it to the ADMETA^{Plus} 2022 Secretariat;

A4 format, up to 2 pages, figures and tables are recommended but not required,

Oral [15 min. talk & 5 min. QA] or Poster [TBD] , Abstracts are due June 30, 2022.

Prospective authors must submit a PDF file up to 2 pages with necessary figures and tables. Send the PDF file of the manuscript, the subject of your paper (please choose from the Conference Topics of Interest), and desired presentation style (Oral or Poster) to the secretariat office via e-mail. On the abstract, please indicate the author to whom correspondence should be addressed, and include mailing and e-mail addresses. A template of abstract can be downloaded from the ADMETA^{Plus} 2022 website. Notification of acceptance will be made to the authors by August 31, 2022. Upon notification, authors will be requested to confirm their participation in the conference.

The accepted abstracts will be included in the on-line conference proceedings, which the participants will receive on site at the registration desk of ADMETA^{Plus} 2022. Pre-registered participants will also be able to download the accepted abstracts from the ADMETA^{Plus} 2022 webpage after Oct. 5, 2022.

QRcode to URL



★ Special Issue of JJAP

Authors with papers presented at ADMETA^{Plus} 2022 are encouraged to submit their original manuscripts to a Special Issue of the Japanese Journal of Applied Physics (JJAP) dedicated to the ADMETA^{Plus} 2022. The deadline for submitting the Special Issue is scheduled around November 2022. The manuscript will be reviewed based on the JJAP standard for an original paper. JJAP Special Issues are accepting regular papers (RP), brief notes (BN), and reviews (RV), and will be published in August, 2023.

★ Reminder

As the papers of the Special Issue will be the original papers, please be careful not to post the same contents to any other journals.

Conference Topics: Oct.13-14, 2022 (in English)

★ General Sessions

- 1) Advanced Integration and Process
- 2) Advanced Metallization
- 3) CMP and Cleaning
- 4) Emerging Technologies (STT-MRAM, ReRAM)
- 5) Next Generation Interconnect (Carbon, Optical Interconnect etc.)
- 6) Thin films and Dielectrics
- 7) 3D Device, TSV and Packaging

Tutorial Topics: Oct.12, 2022 (in Japanese)

★ Tutorial

- 1) CVD/ALD
- 2) Etching
- 3) CMP
- 4) Material Trend
- 5) BEOL Integration and Metallization
- 6) Memory (DRAM or NAND)
- 7) Semiconductor Market Trend
- 8) 3D Packages
- 9) Artificial Intelligence

*Full list of the invited speakers and tutorial lecturers will be announced later.

**Official language of the Tutorial Lecturers is Japanese.

★ Contact: ADMETA^{Plus} 2022 Secretariat

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[Notice from the executive committee] ADMETA^{Plus}2022 is a conference dealing with advanced interconnection technology, and this year marks the 31st time. At the moment, we are planning to hold a hybrid conference. In the past, the abstracts were basically posted on two pages, but now it is possible to post on one page. Even if the space is small, please post a chart that shows the characteristics and originality of your research as much as possible.

In addition, the tutorial in Japanese on advanced LSI wiring technology is also very popular every year. It is used as a place to educate students and young engineers, or as a place to update the latest information. We sincerely look forward to your contributions and participation.

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