

# Call for Papers

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# ADMETA<sup>Plus</sup> 2012

**Advanced Metallization Conference 2012: 22<sup>nd</sup> Asian Session**

**Oct. 22 - 25, 2012**

**The University of Tokyo, Yayoi Auditorium, Ichijo Hall (Hongo Campus), Tokyo**

**Sponsored by The Japan Society of Applied Physics**

Supported by The Surface Finishing Society of Japan, The Institute of Electrical Engineers of Japan, The Japan Society for Precision Engineering, The Institute of Electronics, Information and Communication Engineers, The Japan Institute of Metals, IEEE EDS Japan Chapter, The Vacuum Society of Japan, The Surface Science Society of Japan. (under arrangement)

**Advanced Metallization Conference 2012, 22<sup>nd</sup> Asian Session** (ADMETA<sup>Plus</sup> 2012) will be held from October 22 through 25, 2012. The conference is organized to stimulate and enhance the research and development of ULSI interconnect technology. In every year since 1989, the conference has been held at about the same time in both Japan (ADMETA) and USA (AMC), and has showcased remarkable interconnect technology development in Asia.

Current active development of Cu multilayer wiring technology is aimed at feature sizes of less than 50nm, and application of Cu wiring has been broadened to the memory market and is widely used for Flash and DRAM. Securing good reliability for electromigration and low-k films in Low-k/Cu wiring schemes is still an important area of interest. There have been advancements in the introduction of metal deposition for front end processes such as for Silicide and Metal Gate, and submissions in this area are welcome. The progress of miniaturization technology for packaging is rapid, and concepts for integrated wiring technology between Si chips and mounting substrates are required. 3D wiring technology will be paid special attention as an important direction for further device integration. Contributions of papers addressing these topics are being highly solicited as well as papers in related areas such as interconnect materials, interconnect design, reliability, process equipment, and cost reduction, etc. where there is no shortage of problems to be solved.

In addition to the main conference with its emphasis on the direction of conventional wiring scaling, three Organized Sessions (Backend Devices, CMP, Nanocarbon Interconnects) will be held to discuss post-scaling technology that is either in development or in concept stage.

This conference offers an excellent opportunity to learn about the most recent R&D activities in interconnect technology and related fields from researchers around the world. We look forward to your attendance.

ADMETA<sup>Plus</sup> 2012 General Chair: Hisao Kawasaki (Mitsubishi Heavy Industries)

ADMETA<sup>Plus</sup> 2012 Program Chair: Eiichi Kondo (Yamanashi University)

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## Session Categories

### Conference Topics of Interest

**Integration:** Structure, Performance, Resistance, Capacitance, Evaluation, Analysis, etc.

**Reliability Science and Failure Analysis:** EM, SIV, TDDDB, Measurement, Evaluation, Defect Inspection, Yield Improvement, Process Dispersion Modeling, etc.

**Metallization:** PVD, CVD, ALD, ECD, Barrier Metal, Nucleation Layer, Seed Layer, Alloy, Supercritical, New Materials, etc.

**Low-k Dielectric:** CVD, ALD, SOD, Porous Films, New Materials, Adhesion, Interface Reaction, Air Gap, Evaluation, etc.

**Contact:** Silicide, Interface, Solid Phase Reaction, Shallow Junction, Crystal Properties, Electron Properties, Carrier Transport, Parasitic Resistance, etc.

**3D:** COW, WOW, Thinning, Bonding, TSV, TMV, Bump, Individuating, Redistribution Layer, Cooling, Reliability, etc.

**MEMS/RF:** Structure, Materials, Packaging, Deposition Tech., Etching Tech., CMP, Electroplating, Switch, Inductor, Varactor, Resonator, Transmission Line, etc.

**Emerging Technology:** Active Wiring, Memristor, Sensor, Electronic Luminescence, Silicon Photonics, Power Electronics, Flexible Electronics, Energy Harvesting, etc.

### Organized-Session Topics of Interest

**Backend Devices Session:** Tech. for Embedding Devices (MRAM, PCRAM, ReRAM, DRAM etc.), Science and Process of Magnetic, Phase-Change and Resistive-Change Devices, Electrode, Metallization, etc.

**CMP Session:** Planarization Technology, Slurry, Pad, Dress, End Point Detection, Cleaning, Anti-corrosive Technology, etc.

